



## SPECIAL CALL FOR PAPERS

08 – 10.10.2024 AUSTIN, TEXAS | USA

11 – 13.03.2025 NUREMBERG | GERMANY

The **embedded world Exhibition & Conference** is the world's premier event for the embedded systems industry. It brings together researchers and developers, industry and academia from all disciplines of the embedded systems ecosystem and drives the evolution of complex systems of systems and its manifold innovative aspects.

The **embedded world Conference in collaboration with RISC-V International** is calling for proposals for a special RISC-V session. Abstracts can be submitted both for embedded world 2025 (Nuremberg, Germany) and embedded world North America 2024 (Austin, Texas, USA).

RISC-V is the industry standard RISC Instruction Set Architecture (ISA) driven through open collaboration, enabling freedom of design, and cementing the strategic foundation of semiconductors. With the RISC-V track at the embedded world Conference, we are bringing together the embedded community and their broad variety of hardware, software and system solutions, architectures, and requirements of embedded and IoT development.



The **special RISC-V session topics** shall give an overview of new technology trends. The special topics of the RISC-V session will include:

- Beyond RISC-V ISA: Content includes technical attributes related to the RISC-V ISA including security, performance and optimization, and RASD – reliability, availability, scalability, and diagnosability.
- Software Stack and Development Tools: Novel techniques, solutions, and research that is expanding the RISC-V software stack and providing development tools to support the RISC-V ecosystem.
- Hardware and Processor Design: New innovative approaches to silicon design that take advantage of the customizability and configurability of the RISC-V ISA.
- Industry Targeted Solutions: RISC-V use in end-user products is growing exponentially every year. For this track, we are looking for solutions, research, future trends and product-specific implementations of RISC-V within industries such as IoT or Automotive and application areas including AI / NL and security.

embedded world NA 2024, Austin: [▶ www.embedded-world-na.com/special-cfps](https://www.embedded-world-na.com/special-cfps)

embedded world 2025, Nuremberg: [▶ www.embedded-world.eu/special-cfps](https://www.embedded-world.eu/special-cfps)

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**Important dates** | Submission of abstracts (selecting the topic “11.01 RISC-V”):

- for embedded world North America (8-10 October 2024, Austin, TX, USA) until **14 June 2024**, authors will be notified by 16 August 2024.
- for embedded world 2025 (11-13 March 2025, Nuremberg, Germany) until **27 September 2024**, authors will be notified by early December 2024.